

Specifications of B1z/B1Main Unit

10-28-2009

Item		Main Unit	B1z	B1
Execution Speed			0.38uS/Sequential Instruction	0.38uS/Sequential Instruction
Memory	Program Capacity (Step)		3840	7936
	Element Document (Byte)		4K	8K
Number of I/O	Digital Input X (Point)	6/8/12/14	*1	X+Y=64 *2
	Digital Output Y (Point)	4/6/8/10	*1	X+Y=64 *2
	Analog Input (Channel)			R3992~R3995 (4) *3
	Analog Output (Channel)			R3996~R3997 (2) *3
Internal Relay (M)	Non-retentive (Point)	M0~M511 (512)		M0~M799 (800)
	Retentive (Point)	M512~M767 (256)		M800~M1399 (600)
	Special Relay (Point)	M1912~M2001 (90)		M1912~M2001 (90)
Step Relay (S)	Initialization (Point)	S0~S7 (8)		S0~S7 (8)
	Non-retentive (Point)	S20~S143 (124)		S20~SS499 (480)
	Retentive (Point)	S144~S271 (128)		S500~S999 (500)
Timer	1S	T200~T219 (20)		T200~T219 (20)
	100mS	T50~T113 (64)		T50~T149 (100)
	10mS	T0~T49 (50)		T0~T49 (50)
	1mS	R4151 (1)		R4151 (1)
	Cumulative Timer	FUN87~ FUN89		FUN87~ FUN89
Counter	16-bit Up-count	Retentive	C0~C31 (32)	C0~C47 (48)
		Non-retentive	C32~C63 (32)	C48~C95 (48)
	32-bit Up-count	Retentive	C200~C207 (8)	C200~C215 (16)
		Non-retentive	C208~C215 (8)	C216~C231 (16)
	Up/Down Count	16-bit	FUN7	FUN7
		32-bit	FUN7D	FUN7D
High Speed Counter 1 or 2 HHSC + 4 SHSC	1 Input (CK)	2 channels (10K Hz of each) HSC0 & HSC1 + HSC4~7 (5K Hz)	2 channels (10K Hz of each) HSC0 & HSC1 + HSC4~7 (5K Hz)	
	2 Inputs (UP/DN · CK/DIR)	Or 1channel (10K Hz) HSC0 + HSC4~7 (5K Hz)	Or 1channel (10K Hz) HSC0 + HSC4~7 (5K Hz)	
	2 Inputs (A/B)	Or 1channel (5K Hz) HSC0 + HSC4~7 (5K Hz)	Or 1channel (5K Hz) HSC0 + HSC4~7 (5K Hz)	
Data Register	Retentive	R0~R127 (128)	R0~R2999 (3000)	
	Non-retentive	R128~R511 (384)	R3000~R3839 (840)	
	Special Register	R3840~R4167 (328) R4030~R4057 (Retentive) R4088~R4166 (Retentive)	R3840~R4167 (328) R3968~R4167 (Retentive)	
	Index (Retentive)	V · Z (2)	V · Z (2)	
	Read Only (Retentive)	R5000~R5255 (256)	R5000~R5999 (1000)	
Interrupt Input			X0~X3 (4 Points)	X0~X3 (4 Points)
High Speed Pulse Output (PSO0, PSO1)			Y0~Y3 (4 Points, 10K Hz of each)	Y0~Y3 (4 Points, 10K Hz of each)
Serial Communication port			1	1 (Expandable to 3 ports)
Battery backup for program and data			No (Program and data stored in system FLASH)	Yes

*1 . B1z main unit (B1z-M10/14/20/24), it doesn't support I/O and communication expansion

*2 . B1main unit (B1-M10/14/20/24), it supports I/O expansion, the maximum expansion X (DI) + Y (DO) = 64

*3 . B1 main unit, it supports B1-L2DA/B1-L4AD/B1-L2A1D analog I/O, and communication expansion

*4 . B1z/B1 main unit has the images of digital input X0~X255, and digital output Y0~Y255

*5 . B1z/B1 main unit doesn't support program editing while in RUNmode, also not support Latch Coil -(L) function